

Digital electronics

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Plan

7 lessons/lab sessions (4-hour long schedules):

- 1 Executive environments: principles and introduction, getting started with FreeRTOS
- 2 FreeRTOS, RTEMS, Nuttx ... multitasking and associated methods to make sure shared data and resources are kept in known states (mutex & semaphore)
- 3 Using the scheduler, mutex and semaphores to solve the “philosopher problem”
- 4 Embedded systems with GNU/Linux – POSIX compatible operating system
Architecture of an operating system, kernel v.s userspace
Internet connectivity and networking
- 5 Accessing hardware resources from userspace – memory translation from physical to virtual address space (Memory Management Unit) – /dev/mem
- 6 Accessing hardware resources from a web server – internet connected instrument
- 7 From userspace to kernel space: character device (*char device*) for communicating between users and the kernel

Background: FreeRTOS

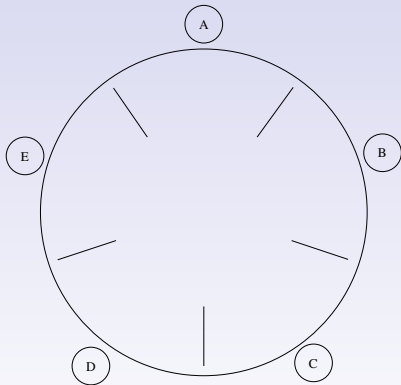
- An executive environment makes you feel you are using an operating system (tasks with priorities and local stack \Rightarrow semaphores and mutex, queues)
- `github.com/jmfriedt/tp_freertos` provides some basic usage examples
- `common` provides basic functionalities such as hardware access as no driver is supported
- `Ono_freertos` provides some basic baremetal C examples to validate that `common` behaves as expected
- `Makefile` assume that FreeRTOS ¹ is located at the same level than `tp_freertos`

¹<https://github.com/FreeRTOS/FreeRTOS-LTS>

Dining philosophers problem

Define **constaints** and leave it to the scheduler to find the solution after providing the transition rules between the state machine

- N philosophers are seated around a table,
- each philosopher has a chopstick at its left and another chopstick at its right,
- each philosopher must grab two chopsticks to eat,
- once a philosopher has eaten, it drops the chopsticks it has used.
- Two philosophers cannot use the same chopstick at the same time.



How can this problem be expressed in the FreeRTOS framework for its scheduler to find the solution?

Dining philosophers problem

- how is a philosopher represented in the FreeRTOS framework?
- how is a chopstick represented in the FreeRTOS framework?
- how would you exit the case where all philosophers have selected to grab the chopstick at their right?

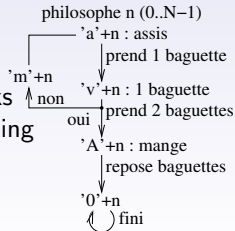
Dining philosophers problem

- how is a philosopher represented in the FreeRTOS framework?
- how is a chopstick represented in the FreeRTOS framework?
- how would you exit the case where all philosophers have selected to grab the chopstick at their right?

Solution example:

eabcdzEwBy4v1xDAoc30xC2

- 1 all philosophers wish to grab a chopstick : eabcd,
- 2 z grabs a chopstick, and then E grabs two chopsticks
- 3 w opposite to {e,Z} grabs one chopstick, then B grabs the second chopstick
- 4 y grabs one chopstick
- 5 4 has finished eating: e drops the chopsticks
- 6 v grabs a chopstick
- 7 1 has finished eating and drops the chopsticks
- 8 x grabs a chopstick, D & A have finished eating and drop their chopsticks
- 9 o is refused the second chopstick and hence drops the first one
- 10 c grabs again one chopstick, ...



Dining philosophers problem: emulator

```
$ qemu-system-arm -M stm32-p103 -serial stdio -kernel output/main.bin
```

```
eabcdEBung
```

```
wng
```

```
4D1A03C2
```

qemu supporting the STM32F1:

https://beckus.github.io/qemu_stm32/

See hw/arm: stm32_p103.c platform

Adding a serial port:

```
DeviceState *uart3 = DEVICE(object_resolve_path("/machine/→  
    ↪stm32/uart [3] ", NULL));  
assert(uart3);  
stm32_uart_connect((Stm32Uart *)uart3, serial_hds[0], →  
    ↪STM32_USART3_NO_REMAP);
```

Solution depending on scheduler settings

Different scheduling schemes depending on rules (timeout mutex = 500 ms):

- no delay (no one returns a chopstick, no delay between first and second mutex)
ebcdazEwBy4Dv1xA3C02
- 200 ms delay (all philosophers have returned their chopsticks while only a single one is eating)
eabcdzvwxyqeDmanboc3zvwxCqemanb2zvwBqema1zvAqe0zE4
- 500 ms delay (two philosophers at opposite positions on the table eat at the same time)
ebcdazwxyvqeDnbocA3zx0wEC42B1
- 800 ms delay (same as 200 ms)
eabcdzvwxyqeDmanboc3zvwxCqemanb2zvwBqema1zvAqe0zE4

Emulator: using an un-initialized peripheral

Message error from the emulator when an un-initialized is used:

```
USART_InitTypeDef USART_InitStructure;  
...  
USART_Init(USART2, &USART_InitStructure);  
USART_Cmd(USART2, ENABLE);  
put_char(USART1, '0');  
put_char(USART2, '0');
```

We attempt using serial port 1 which was never initialized \Rightarrow neither clock source nor configuration

```
$ qemu_stm32/arm-softmmu/qemu-system-arm -M stm32-p103 -serial stdio -serial stdio -serial stdio -kernel main.bin  
qemu stm32: hardware warning: Warning: You are attempting to use the UART1 peripheral while its clock is disabled.  
  
R00=40013800 R01=00000030 R02=008e0001 R03=00000030  
R04=20004fe8 R05=08000b1c R06=00000000 R07=20004fc0  
R08=00000000 R09=00000000 R10=00000000 R11=00000000  
R12=0000000f R13=20004fc0 R14=08000519 R15=08000808  
PSR=20000173 --C- T svc32  
qemu: hardware error: Attempted to write to USART_DR while UART was disabled.  
CPU #0:  
R00=40013800 R01=00000030 R02=008e0001 R03=00000030  
R04=20004fe8 R05=08000b1c R06=00000000 R07=20004fc0  
R08=00000000 R09=00000000 R10=00000000 R11=00000000  
R12=0000000f R13=20004fc0 R14=08000519 R15=08000808  
PSR=20000173 --C- T svc32  
FPSCR: 00000000  
Aborted
```

Access to shared resources

- Messages were so far atomic (single letter) to avoid interferences between multiples messages sent by philosophers.
- `qemu-system-arm -M stm32-p103 -serial stdio -serial stdio -serial stdio -kernel output/main.bin`

```
LED Off                2 drops chopsticks
4 wants to eat         2 wants to eat
0 wants to eat         3 has eaten
1 wants to eat         4 has grabbed one chopstick
2 wants to eat         4 has grabbed two chopsticks
3 wants to eat         1 has grabbed one chopstick
4 has grabbed one chopstick 2 has grabbed one chopstick
0 has grabbed one chopstick 2 has grabbed two chopsticks
0 has grabbed two chopsticks 4 has eaten
2 has grabbed one chopstick 1 drops chopsticks
3 has grabbed one chopstick 1 wants to eat
4 drops chopsticks       2 has eaten
4 wants to eat           1 has grabbed one chopstick
3 has grabbed two chopsticks 1 has grabbed two chopsticks
1 wants to eat           1 has eaten
0 has eaten
```

- How many mutex are needed to synchronize access to the serial port?
- What are the consequences of removing the mutex?

Conclusion

- 1 an emulator allows for completing the job even without hardware platform
- 2 an emulator provides some hint at the internal state of the processor and prevents the user from making mistakes ...
- 3 ... assuming the peripheral is properly emulated.

ADC ², DAC, timer, GPIO, USART fonctionnal for STM32

Eclipse includes an emulator for the STM32F4:

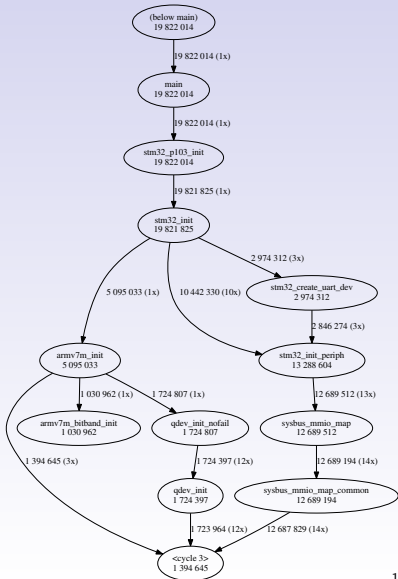
<http://gnuarmeclipse.github.io/qemu/> (STM32F4-Discovery)

²error in ADC flag handing detected when using libopencm3 which tests ADC_CR2_SWSTART bit of ADC_CR2 to check that conversion has started: this bit should be set to 0 upon conversion start:

https://github.com/beckus/qemu_stm32/issues/24

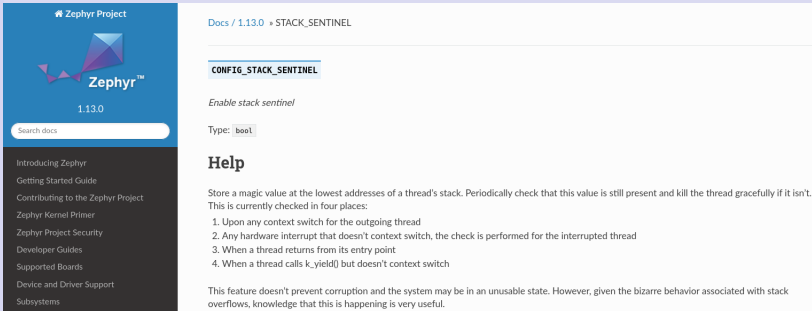
Analyzing the execution of an emulator

- qemu based on callback functions called when an event occurs requesting the emulation of a peripheral
- ⇒ sequence of executed functions hard to follow as they are not sequentially explicitly called
- `valgrind --tool=callgrind -v \`
`--dump-every-bb=10000000 \`
`../qemu-system-arm \`
`-M stm32-p103 \`
`-serial stdio \`
`-kernel temperature/main.bin`
- `kcachegrind callgrind.out.8964` displays a chart of the called functions and the associated resources.



Beyond FreeRTOS ...

Many more executive environments to explore and discover, with more created (and dying) every day.



The screenshot shows the Zephyr Project documentation page for the `CONFIG_STACK_SENTINEL` configuration option. The page is titled "Docs / 1.13.0 » STACK_SENTINEL" and includes a search bar with the text "Search docs". The left sidebar contains a navigation menu with items such as "Introducing Zephyr", "Getting Started Guide", "Contributing to the Zephyr Project", "Zephyr Kernel Primer", "Zephyr Project Security", "Developer Guides", "Supported Boards", "Device and Driver Support", and "Subsystems". The main content area features the heading "CONFIG_STACK_SENTINEL" and the text "Enable stack sentinel". Below this, the type is listed as "Type: bool". A "Help" section follows, explaining that the feature stores a magic value at the lowest addresses of a thread's stack and periodically checks its presence. It lists four places where this check is performed: 1. Upon any context switch for the outgoing thread, 2. Any hardware interrupt that doesn't context switch, 3. When a thread returns from its entry point, and 4. When a thread calls `k_yield()` but doesn't context switch. A note at the bottom states that while this feature doesn't prevent corruption, it is useful for debugging.

↑ Example of stack debugging functionality provided by Zephyr Project.

Introduction to NuttX [in French]: G. Goavec-Merou, J.-M. Friedt, *Un environnement exécutif visant la compatibilité POSIX : NuttX pour contrôler un analyseur de réseau à base de STM32*, GNU/Linux Magazine France (Dec. 2017) at http://jmfriedt.free.fr/lm_nuttX.pdf